

IN THE SPECIFICATION

1. Please amend the paragraph starting on page 30, line 12 of the application as follows:

In addition, transaction control block 256 (of FIGURE 10) outputs a chip select 276 for ~~typical~~ functional module 241 or configuration 215 and receives and transmits internal control signals (for example, read/write, or direct memory access (“DMA”) acknowledge indication) 275.

2. Please amend the paragraph starting on page 30, line 17 of the application as follows:

FIGURE 12 shows transaction control block 256, according to an advantageous embodiment of the present invention. A comparator 304 compares the addresses 262 (higher bits) received from host interface 212 with a stored list of addresses (base + size) 263 corresponding to modules 241 or configuration 215. Based on the comparison, comparator 304 generates a host chip select 312 for ~~typical~~ functional module 241 or configuration 215.